

©⊒#Search Results

BROWSE

SEARCH

IEEE XPLORE GUIDE

SUPPORT

e-mail 🚇 printer triendly

Results for "(('way pointer')<in>metadata)"

Your search matched 0 documents.

A maximum of 100 results are displayed, 25 to a page, sorted by Relevance in Descending order.

» Search Options

IEEE JNL

IEEE Journal or Magazine

IEE JNL

IEE Journal or Magazine

IEEE CNF

IEEE Conference Proceeding
IEE Conference Proceeding

IEE CNF

122 00:110:01:00 / 1000

IEEE Standard

No results were found.

Please edit your search criteria and try again. Refer to the Help pages if you need assistance revising your search.

Indexed by

Help Contact Us Privacy & Security IEEE.org



IEEE XPLORE GUIDE Search Results **BROWSE** SEARCH SUPPORT Results for "(('victim way')<in>metadata)" e-mail Aprinter triendly Your search matched 0 documents. A maximum of 100 results are displayed, 25 to a page, sorted by Relevance in Descending order. » Search Options View Session History **Modify Search** (('victim way')<in>metadata) New Search **>>** Check to search only within this results set » Кеу Display Format: Citation Citation & Abstract IEEE JNL IEEE Journal or Magazine IEE JNL IEE Journal or Magazine IEEE CNF IEEE Conference Proceeding No results were found. IEE CNF IEE Conference Proceeding Please edit your search criteria and try again. Refer to the Help pages if you need assistance revising your search. IEEE STD IEEE Standard

Indexed by Inspect

Help Contact Us Privacy & Security IEEE.org



B®Search Results

BROWSE SEARCH **IEEE XPLORE GUIDE**

>>

SUPPORT

e-mail Aprinter triendly

Results for "(('trace cache' <and> pointer)<in>metadata)"

Your search matched 1 of 1302021 documents.

A maximum of 100 results are displayed, 25 to a page, sorted by Relevance in Descending order.

» Search Options

» Key

IEEE CNF

IEEE STD

View Session History **Modify Search** (('trace cache' <and> pointer)<in>metadata) New Search

IEEE Conference Proceeding

IEEE JNL IEEE Journal or Magazine

IEE JNL IEE Journal or Magazine

IEE CNF IEE Conference Proceeding

IEEE Standard

Display Format:

Check to search only within this results set

Citation

Citation & Abstract

1. The block-based trace cache

Black, B.; Rychlik, B.; Shen, J.P.;

Computer Architecture, 1999. Proceedings of the 26th International Symposium on

2-4 May 1999 Page(s):196 - 207

Digital Object Identifier 10.1109/ISCA.1999.765951

AbstractPlus | Full Text: PDF(192 KB) IEEE CNF

Indexed by #Inspec Contact Us Privacy & Security IEEE.org



IEE Journal or Magazine

IEEE Conference Proceeding

IEE Conference Proceeding

IEEE Standard

No results were found.

Welcome United States Patent and Trademark Office

BROWSE IEEE XPLORE GUIDE SUPPORT ⊌⊠≱Search Results SEARCH Results for "(('trace cache' <and> 'victim way')<in>metadata)" e-mail printer triendly Your search matched 0 documents. A maximum of 100 results are displayed, 25 to a page, sorted by Relevance in Descending order. » Search Options View Session History **Modify Search** (('trace cache' <and> 'victim way')<in>metadata) **New Search >>** Check to search only within this results set » Key Citation Citation & Abstract IEEE JNL IEEE Journal or Magazine

Please edit your search criteria and try again. Refer to the Help pages if you need assistance revising your search.

Indexed by

IEE JNL

IEEE CNF

IEE CNF

IEEE STD

Help Contact Us Privacy & Security IEEE.org



Search Results		-	BROWSE		SEARCH	IEEE XPLORE GUIDE		SUPPORT	
Your search	((trace cache) <in>metadata)" matched 54 of 1302021 document of 100 results are displayed, 25 to</in>		d by	Relevance in Descending orde	er.		⊠ e-mail	Eprinter friendly	
» Search Opt	ions	Modif	/ Sea	arch					
View Session	n History		Modify Search ((trace cache) <in>metadata) >></in>						
New Search	•								
		<u> </u>	Chec	k to search only within this resul	its set				
» Key		Displa	y Fo	ormat: © Citation	Citation & Abstra	act			
IEEE JNL	IEEE Journal or Magazine	Select	A	rticle Information			View	v: 1-25 <u>26-50</u> <u>51-54</u>	
IEE JNL	IEE Journal or Magazine								
IEEE CNF	IEEE Conference Proceeding		1.	. Filtering techniques to improve trace-cache efficiency					
IEE CNF	IEE Conference Proceeding			Rosner, R.; Mendelson, A.; Ronen, R.; Parallel Architectures and Compilation Techniques, 2001. Proceedings. 2001 International Conference on					
IEEE STD	IEEE Standard			8-12 Sept. 2001 Page(s):37 - 4					
				Digital Object Identifier 10.110 <u>AbstractPlus</u> Full Text: <u>PDF(</u>					
				ADSTRUCTION TOTAL TOTAL	1000 KB) IEEE CHI				
			2.	Trace cache redundancy: re- Ramirez, A.; Larriba-Pey, J.Ll. High-Performance Computer & 8-12 Jan. 2000 Page(s):325 - Digital Object Identifier 10.110	; Valero, M.; Architecture, 2000. HPC 333	CA-6. Proceedings. Sixth Internati	ional Sympos	sium on	
				AbstractPlus Full Text: PDF(104 KB) IEEE CNF				
		□	3.	Using dynamic branch beha Hu, J.S.; Vijaykrishnan, N.; Irw VLSI, 2003. Proceedings. IEEI 20-21 Feb. 2003 Page(s):127 AbstractPlus Full Text: PDF(rin, M.J.; Kandemir, M.; E Computer Society An - 132				
			4.	The effect of program optimi Howard, D.L.; Lipasti, M.H.; Parallel Architectures and Con 12-16 Oct. 1999 Page(s):256 - Digital Object Identifier 10.110 AbstractPlus Full Text: PDF(npilation Techniques, 19 261 9/PACT.1999.807570	efficiency 999. Proceedings. 1999 Internation	onal Confere	nce on	
			5.	Trace Cache Sampling Filter Behar, M.; Mendelson, A.; Kole Parallel Architectures and Con 17-21 Sept. 2005 Page(s):255 Digital Object Identifier 10.110 AbstractPlus Full Text: PDF(1)	odny, A.; npilation Techniques, 20 - 266 9/PACT.2005.38	005. PACT 2005. 14th Internation	nal Conferenc	ce on	
			6.	Trace preconstruction Jacobson, Q.; Smith, J.E.; Computer Architecture, 2000. I 2000 Page(s):37 - 46 AbstractPlus Full Text: PDF(S	-	n International Symposium on			

7. The block-based trace cache Black, B.; Rychlik, B.; Shen, J.P.; Computer Architecture, 1999. Proceedings of the 26th International Symposium on 2-4 May 1999 Page(s):196 - 207 Digital Object Identifier 10.1109/ISCA.1999.765951 AbstractPlus Full Text: PDF(192 KB) IEEE CNF
8. Evaluating trace cache on moderate-scale processors Sato, T.; Computers and Digital Techniques, IEE Proceedings- Volume 147, Issue 6, Nov. 2000 Page(s):369 - 374 Digital Object Identifier 10.1049/ip-cdt:20000889 AbstractPlus Full Text: PDF(532 KB) IEE JNL
9. Improving trace cache effectiveness with branch promotion and trace packing Patel, S.J.; Evers, M.; Patt, Y.N.; Computer Architecture, 1998. Proceedings. The 25th Annual International Symposium on 27 June-1 July 1998 Page(s):262 - 271 Digital Object Identifier 10.1109/ISCA.1998.694786 AbstractPlus Full Text: PDF(92 KB) IEEE CNF
10. Alternative fetch and Issue policies for the trace cache fetch mechanism Friendly, D.H.; Sanjay Jeram Patel; Patt, Y.N.; Microarchitecture, 1997. Proceedings. Thirtieth Annual IEEE/ACM International Symposium on 1-3 Dec. 1997 Page(s):24 - 33 Digital Object Identifier 10.1109/MICRO.1997.645794 AbstractPlus Full Text: PDF(912 KB) IEEE CNF
11. A mathematical model of trace cache Hossain, A.; Pease, D.J.; Burns, J.S.; Parveen, N.; Application-Specific Systems, Architectures and Processors, 2002. Proceedings. The IEEE International Conference on 17-19 July 2002 Page(s):151 - 162 Digital Object Identifier 10.1109/ASAP.2002.1030715 AbstractPlus Full Text: PDF(320 KB) IEEE CNF
12. Power-efficient trace caches Hu, J.S.; Vijaykrishnan, N.; Kandemir, A.; Irwin, A.J.; Design, Automation and Test in Europe Conference and Exhibition, 2002. Proceedings 4-8 March 2002 Page(s):1091 Digital Object Identifier 10.1109/DATE.2002.999209 AbstractPlus Full Text: PDF(274 KB) IEEE CNF
13. Evaluation of design options for the trace cache fetch mechanism Patel, S.J.; Friendly, D.H.; Patt, Y.N.; Computers, IEEE Transactions on Volume 48, Issue 2, Feb. 1999 Page(s):193 - 204 Digital Object Identifier 10.1109/12.752661 AbstractPlus References Full Text: PDF(948 KB) IEEE JNL
14. Improving dynamic cluster assignment for clustered trace cache processors Ravi Bhargava; John, L.K.; Computer Architecture, 2003. Proceedings. 30th Annual International Symposium on 9-11 June 2003 Page(s):264 - 274 Digital Object Identifier 10.1109/ISCA.2003.1207006 AbstractPlus Full Text: PDF(295 KB) IEEE CNF
15. Trace Cache performance parameters Hossain, A.; Pease, D.J.; Burns, J.S.; Parveen, N.; Computer Design: VLSI in Computers and Processors, 2002. Proceedings. 2002 IEEE International Conference on 16-18 Sept. 2002 Page(s):348 - 355 Digital Object Identifier 10.1109/ICCD.2002.1106793

16. Completion time multiple branch prediction for enhancing trace cache performance Rakvic, R.; Black, B.; Shen, J.P.; Computer Architecture, 2000. Proceedings of the 27th International Symposium on 2000 Page(s):47 - 58
AbstractPlus Full Text: PDF(924 KB) IEEE CNF
17. Dynamic profiling and trace cache generation Berndl, M.; Hendren, L.; Code Generation and Optimization, 2003. CGO 2003. International Symposium on 23-26 March 2003 Page(s):276 - 285 Digital Object Identifier 10.1109/CGO.2003.1191552 AbstractPlus Full Text: PDF(310 KB) IEEE CNF
18. A trace cache microarchitecture and evaluation Rotenberg, E.; Bennett, S.; Smith, J.E.; Computers, IEEE Transactions on Volume 48, Issue 2, Feb. 1999 Page(s):111 - 120 Digital Object Identifier 10.1109/12.752652 AbstractPlus References Full Text: PDF(788 KB) IEEE JNL
19. On augmenting trace cache for high-bandwidth value prediction Sang-Jeong Lee; Pen-Chung Yew; Computers, IEEE Transactions on Volume 51, Issue 9, Sept. 2002 Page(s):1074 - 1088 Digital Object Identifier 10.1109/TC.2002.1032626 AbstractPlus References Full Text: PDF(4378 KB) IEEE JNL
20. Software Trace Cache Ramirez, A.; Larriba-Pey, J.L.; Valero, M.; Computers, IEEE Transactions on Volume 54, Issue 1, Jan 2005 Page(s):22 - 35 Digital Object Identifier 10.1109/TC.2005.13 AbstractPlus Full Text: PDF(1032 KB) IEEE JNL
21. Energy-aware fetch mechanism: trace cache and BTB customization Chaver, D.; Rojas, M.A.; Pinuel, L.; Prieto, M.; Tirado, F.; Huang, M.C.; Low Power Electronics and Design, 2005. ISLPED '05. Proceedings of the 2005 International Symposium or 8-10 Aug. 2005 Page(s):42 - 47 AbstractPlus Full Text: PDF(389 KB) IEEE CNF
22. Analysis of control flow patterns in the execution of SPEC CPU2000 benchmark programs Joseph, P.J.; Matthew Jacob, T.; TENCON 2003. Conference on Convergent Technologies for Asia-Pacific Region Volume 3, 15-17 Oct. 2003 Page(s):1143 - 1147 Vol.3 Digital Object Identifier 10.1109/TENCON.2003.1273426 AbstractPlus Full Text: PDF(398 KB) IEEE CNF
23. Fetching Instruction streams Ramirez, A.; Santana, O.J.; Larriba-Pey, J.L.; Valero, M.; Microarchitecture, 2002. (MICRO-35). Proceedings. 35th Annual IEEE/ACM International Symposium on 18-22 Nov. 2002 Page(s):371 - 382 Digital Object Identifier 10.1109/MICRO.2002.1176264 AbstractPlus Full Text: PDF(268 KB) IEEE CNF
24. Out-of-order Instruction fetch using multiple sequencers Oberoi, P.; Sohi, G.; Parallel Processing, 2002. Proceedings. International Conference on 18-21 Aug. 2002 Page(s):14 - 23 Digital Object Identifier 10.1109/ICPP.2002.1040855

AbstractPlus | Full Text: PDF(279 KB) | IEEE CNF

25. Trace cache: a low latency approach to high bandwidth instruction fetching
Rotenberg, E.; Bennett, S.; Smith, J.E.;
Microarchitecture, 1996. MICRO-29. Proceedings of the 29th Annual IEEE/ACM International Symposium on
2-4 Dec. 1996 Page(s):24 - 34
Digital Object Identifier 10.1109/MICRO.1996.566447

AbstractPlus | Full Text: PDF(1236 KB) IEEE CNF

Indexed by

Help Contact Us Privacy & Security IEEE.org

© Copyright 2005 IEEE – All Rights Reserved

>>



Welcome United States Patent and Trademark Office

BROWSE SEARCH IEEE XPLORE GUIDE

☑e-mail 🖶 printer friendly

SUPPORT

Results for "((((trace cache)<in>metadata))<and>(way pointer<in>metadata))"

Your search matched 0 of 54 documents.

A maximum of 100 results are displayed, 25 to a page, sorted by Relevance in Descending order.

» Search Options

View Session History

-

New Search

» Key

IEEE JNL

IEEE Journal or Magazine

IEE JNL

IEE Journal or Magazine

IEEE CNF

IEEE Conference Proceeding

IEE CNF

IEE Conference Proceeding

IEEE STD IEEE Standard

Modify Search

Display Format:

((((trace cache)<in>metadata))<and>(way pointer<in>metadata))

Check to search only within this results set

Citation

Citation & Abstract

No results were found.

Please edit your search criteria and try again. Refer to the Help pages if you need assistance revising your search.

Indexed by

Help Contact Us Privacy & Security IEEE.org



ଅଞ୍ୟSearch Results		BROWSE	SEARCH	IEEE XPLORE GUIDE	SUPPORT			
Your search	"((trace cache) <in>metadata)" matched 54 of 1302021 document of 54 results are displayed, 25 to a</in>		by Relevance in Descending order	er.		e-mail 🚇 printer triendly		
» Search Op	tions	Modif	y Search					
View Sessio	n History	((trace	((trace cache) <in>metadata)</in>					
New Search		Check to search only within this results set						
		Displa	ay Format:	Citation & Abstra	act			
» Key								
IEEE JNL	IEEE Journal or Magazine	Select	Article Information			View: 1-25 26-50 51-54		
IEE JNL	IEE Journal or Magazine							
IEEE CNF	IEEE Conference Proceeding		26. Towards Pairing Java Applications on SMT Processors Wei Huang; Jiang Lin; Zhao Zhang; Chang, J.M.; Modeling, Analysis, and Simulation of Computer and Telecommunication Systems, 2005. 13th IEEE International					
IEE CNF	IEE Conference Proceeding							
IEEE STD	IEEE Standard		Symposium on 27-29 Sept. 2005 Page(s):7 Digital Object Identifier 10.11					
			AbstractPlus Full Text: PDF	(616 KB) IEEE CNF				
				M.; Chappell, T.; Fletche ce, 2001. Digest of Tech 325, 461	tz integer execution unit er, T.D.; Samaan, S.; Murray, R.; nical Papers. ISSCC. 2001 IEEE	International		
			AbstractPlus Full Text: PDF	(188 KB) IEEE CNF				
			28. A hardware mechanism for Merten, M.C.; Trick, A.R.; Ny Computer Architecture, 2000 2000 Page(s):59 - 70	strom, E.M.; Barnes, R.D		s		
			AbstractPlus Full Text: PDF	(1276 KB) IEEE CNF				
			29. One billion transistors, one Patt, Y.N.; Patel, S.J.; Evers, Computer Volume 30, Issue 9, Sept. 1 Digital Object Identifier 10.11 AbstractPlus References F	M.; Friendly, D.H.; Stark 997 Page(s):51 - 57 09/2.612249	, J.;			
			 A 0.18-μm CMOS IA-32 pro Hinton, G.; Upton, M.; Sager, Sprague, M.; Samaan, S.; Mi Solid-State Circuits, IEEE Jo Volume 36, Issue 11, Nov. 2 Digital Object Identifier 10.11 	D.J.; Boggs, D.; Carmea urray, R.; urnal of 001 Page(s):1617 - 162	an, D.M.; Roussel, P.; Chappell, 1	T.I.; Fletcher, T.D.; Milshtein, M.S.;		
			AbstractPlus References F	ull Text: <u>PDF(</u> 316 KB)	IEEE JNL			
		屬	31. Power awareness through: Rosper R : Almon Y : Moffie	•	•			

19-23 June 2004 Page(s):162 - 173

Computer Architecture, 2004. Proceedings. 31st Annual International Symposium on

Digital Object Identifier 10.1109/ISCA.2004.1310772 AbstractPlus | Full Text: PDF(357 KB) IEEE CNF 32. Distributing the frontend for temperature reduction Chaparro, P.; Magklis, G.; Gonzalez, J.; Gonzalez, A.; High-Performance Computer Architecture, 2005. HPCA-11. 11th International Symposium on 12-16 Feb. 2005 Page(s):61 - 70 Digital Object Identifier 10.1109/HPCA.2005.12 AbstractPlus | Full Text: PDF(344 KB) IEEE CNF 33. Designing a runtime reconfigurable processor for general purpose applications Niyonkuru, A.; Zeidler, H.C.; Parallel and Distributed Processing Symposium, 2004. Proceedings. 18th International 26-30 April 2004 Page(s):143 Digital Object Identifier 10.1109/IPDPS.2004.1303123 AbstractPlus | Full Text: PDF(1329 KB) IEEE CNF 34. Specialized dynamic optimizations for high-performance energy-efficient microarchitecture Almog, Y.; Rosner, R.; Schwartz, N.; Schmorak, A.; Code Generation and Optimization, 2004. CGO 2004. International Symposium on 2004 Page(s):137 - 148 Digital Object Identifier 10.1109/CGO.2004.1281670 AbstractPlus | Full Text: PDF(375 KB) IEEE CNF 35. Latency tolerant branch predictors Santana, O.J.; Ramirez, A.; Valero, M.; Innovative Architecture for Future Generation High-Performance Processors and Systems, 2003 17 July 2003 Page(s):30 - 39 Digital Object Identifier 10.1109/IWIA.2003.1262780 AbstractPlus | Full Text: PDF(296 KB) | IEEE CNF 36. Instruction usage and the memory gap problem

· -	Fernandes, E.S.T.; Barbosa, V.C.; Ramos, F.; Computer Architecture and High Performance Computing, 2002. Proceedings. 14th Symposium on
	28-30 Oct. 2002 Page(s):169 - 175
	Digital Object Identifier 10.1109/CAHPC.2002.1180773
	AbstractPlus Full Text: PDF(428 KB) IEEE CNF
	37. Instruction fetch deferral using static slack
	Muthler, G.A.; Crowe, D.; Patel, S.J.; Lumetta, S.S.;
	Microarchitecture, 2002. (MICRO-35). Proceedings. 35th Annual IEEE/ACM International Symposium on 18-22 Nov. 2002 Page(s):51 - 61
	Digital Object Identifier 10.1109/MICRO.2002.1176238
	AbstractPlus Full Text: PDF(392 KB) IEEE CNF
	38. An analytical model for trace cache instruction fetch performance
-	Hossain, A.; Pease, D.J.;
	Computer Design, 2001. ICCD 2001. Proceedings. 2001 International Conference on
	23-26 Sept. 2001 Page(s):477 - 480
	Digital Object Identifier 10.1109/ICCD.2001.955069
	AbstractPlus Full Text: PDF(256 KB) IEEE CNF
	39. eXtended block cache
	Jourdan, S.; Rappoport, L.; Almog, Y.; Erez, M.; Yoaz, A.; Ronen, R.; High-Performance Computer Architecture, 2000. HPCA-6. Proceedings. Sixth International Symposium on
	8-12 Jan. 2000 Page(s):61 - 70
	Digital Object Identifier 10.1109/HPCA.2000.824339
	AbstractPlus Full Text: PDF(104 KB) IEEE CNF
П	40.
•	PipeRench Implementation of the Instruction Path Coprocessor
archre	sult.jsp?queryText=%28%28trace+cache%29%3Cin%3Emetadata%2

Microarchitecture, 2000. MICRO-33. Proceedings. 33rd Annual IEEE/ACM International Symposium on 10-13 Dec. 2000 Page(s):147 - 158 Digital Object Identifier 10.1109/MICRO.2000.898066 AbstractPlus | Full Text: PDF(956 KB) | IEEE CNF 41. Microprocessors for the years 2001 and 2008: Where will we be in 2001? What will still need to be done for 2008? П Integrated Circuits and Systems Design, 1999. Proceedings. XII Symposium on 29 Sept.-2 Oct. 1999 Page(s):2 Digital Object Identifier 10.1109/SBCCI.1999.802955 AbstractPlus | Full Text: PDF(4 KB) | IEEE CNF 42. Optimization of instruction fetch for decision support workloads Ramirez, A.; Larriba-Pey, J.L.; Navarro, C.; Serrano, X.; Valero, M.; Torrellas, J.; Parallel Processing, 1999. Proceedings. 1999 International Conference on 21-24 Sept. 1999 Page(s):238 - 245 Digital Object Identifier 10.1109/ICPP.1999.797409 AbstractPlus | Full Text: PDF(92 KB) | IEEE CNF 43. Putting the fill unit to work: dynamic optimizations for trace cache microprocessors Friendly, D.H.; Patel, S.J.; Patt, Y.N.; Microarchitecture, 1998. MICRO-31. Proceedings. 31st Annual ACM/IEEE International Symposium on 30 Nov.-2 Dec. 1998 Page(s):173 - 181 Digital Object Identifier 10.1109/MICRO.1998.742779 AbstractPlus | Full Text: PDF(96 KB) | IEEE CNF 44. Aggressive dynamic execution of multimedia kernel traces П Bishop, B.; Owens, R.; Irwin, M.J.; Parallel Processing Symposium, 1998. 1998 IPPS/SPDP. Proceedings of the First Merged International...and Symposium on Parallel and Distributed Processing 1998 30 March-3 April 1998 Page(s):640 - 646 Digital Object Identifier 10.1109/IPPS.1998.669994 AbstractPlus | Full Text: PDF(640 KB) IEEE CNF 45. Memory hierarchy limitations in multiple-instruction-issue processor design Vintan, L.: Steven, G.: EUROMICRO 97. 'New Frontiers of Information Technology'. Short Contributions., Proceedings of the 23rd Euromicro Conference 1-4 Sept. 1997 Page(s):252 - 257 Digital Object Identifier 10.1109/EMSCNT.1997.658474 AbstractPlus | Full Text: PDF(396 KB) IEEE CNF 46. Path-based next trace prediction Jacobson, Q.; Rotenberg, E.; Smith, J.E.; Microarchitecture, 1997. Proceedings. Thirtieth Annual IEEE/ACM International Symposium on 1-3 Dec. 1997 Page(s):14 - 23 Digital Object Identifier 10.1109/MICRO.1997.645793 AbstractPlus | Full Text: PDF(872 KB) | IEEE CNF 47. Superspeculative microarchitecture for beyond AD 2000 П Lipasti, M.H.; Shen, J.P.; Volume 30, Issue 9, Sept. 1997 Page(s):59 - 66 Digital Object Identifier 10.1109/2.612250 AbstractPlus | References | Full Text: PDF(336 KB) IEEE JNL Microarchitectural innovations: boosting microprocessor performance beyond semiconductor technology scaling Moshovos, A.; Sohi, G.S.; Proceedings of the IEEE

Yuan Chou; Pillai, P.; Schmit, H.; Shen, J.P.;

Volume 89, Issue 11, Nov. 2001 Page(s):1560 - 1575 Digital Object Identifier 10.1109/5.964438

AbstractPlus | References | Full Text: PDF(196 KB) | Full Text: HTML | IEEE JNL

49. Application-Specific Processing on a General-Purpose Core via Transparent Instruction Set Customization

Clark, N.; Kudlur, M.; Hyunchul Park; Mahlke, S.; Flautner, K.; Microarchitecture, 2004. MICRO-37 2004. 37th International Symposium on

04-08 Dec. 2004 Page(s):30 - 40

Digital Object Identifier 10.1109/MICRO.2004.5

AbstractPlus | Full Text: PDF(304 KB) IEEE CNF

50. Heat stroke: power-density-based denial of service in SMT

Hasan, J.; Jalote, A.; Vijaykumar, T.N.; Brodley, C.E.; High-Performance Computer Architecture, 2005. HPCA-11. 11th International Symposium on

12-16 Feb. 2005 Page(s):166 - 177

1

Digital Object Identifier 10.1109/HPCA.2005.16

AbstractPlus | Full Text: PDF(160 KB) IEEE CNF

View: 1-25 | 26-50 | 51-54

Help Contact Us Privacy & Security IEEE.org

